Triacs logic level
BT136-600

GENERAL DESCRIPTION

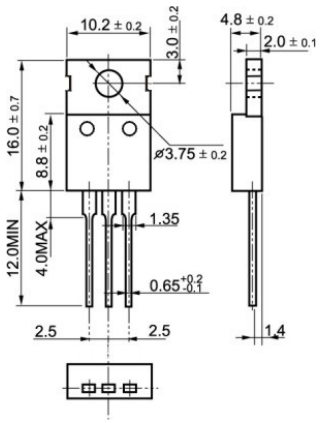
Passivated, sensitive gate triac in a plastic envelope, intended for use in general purpose bidirectional switching and phase control applications. This device is intended to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits.

ABSOLUTE MAXIMUM RATINGS (Ta = 25℃)

Parameter	Symbol	Typ	Unit
Repetitive peak off-state voltages	V_{DRM} V_{RRM}	600	V
RMS on-state current	$I_{T(RMS)}$	4.0	A
Non-repetitive peak on-state current	I_{TSM}	25	A
Max. Operating Junction Temperature	T_j	110	℃
Storage Temperature	T_{stg}	-45~150	℃

1 2 3

1: T1
2: T2
3: G
Tab:T2



TO-220

ELECTRICAL CHARACTERISTICS (Ta = 25℃)

Parameter		Symbol	Test Conditions	Min	Typ	Max	Unit
Repetitive peak off-state voltages		V_{DRM} V_{RRM}	I_D=0.1mA	600	—	—	V
RMS on-state current		$I_{T(RMS)}$	full sine wave; T_{mb} ≤107 ℃	—	4.0	—	A
On-state voltage		V_T	I_T= 5.0A	—	1.4	1.7	V
Holding current		I_H	V_D = 12 V; I_{GT} = 0.1 A	—	2.2	15	mA
Gate trigger current	T2+G+	I_{GT}	V_D = 12 V; I_T = 0.1 A	—	2.5	10	mA
	T2+G-			—	4.0	10	
	T2-G-			—	5.0	10	
	T2-G+			—	11	25	
Latching current	T2+G+	I_L	V_D = 12 V; I_{GT} = 0.1 A	—	3.0	15	mA
	T2+G-			—	10	20	
	T2-G-			—	2.5	15	
	T2-G+			—	4.0	20	
Gate trigger voltage		V_{GT}	V_D = 12 V; I_T = 0.1 A	—	0.7	1.5	V