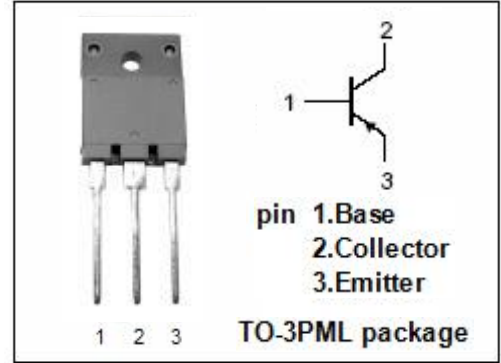


isc Silicon PNP Power Transistor
2SB966
DESCRIPTION

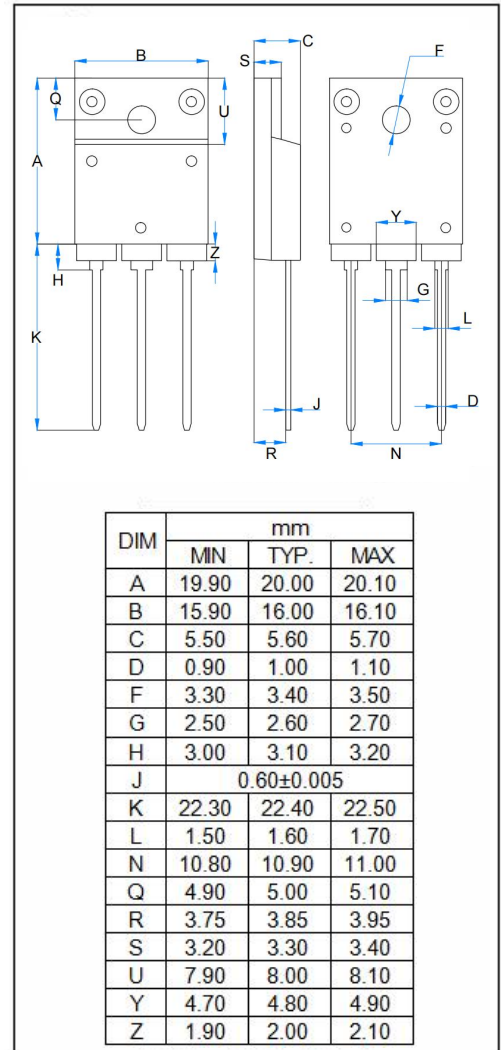
- Low Collector Saturation Voltage
: $V_{CE(sat)} = -0.65V(Typ) @ I_C = -5.0A$
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = -120V(Min)$
- Complement to Type 2SD1289
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation


APPLICATIONS

- Audio frequency power amplifier applications

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-120	V
V_{CEO}	Collector-Emitter Voltage	-120	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-8	A
I_{CP}	Collector Current-Pulse	-12	A
P_C	Total Power Dissipation @ $T_c=25^\circ C$	80	W
T_J	Junction Temperature	150	°C
T_{stg}	Storage Temperature Range	-55~150	°C



isc Silicon PNP Power Transistor

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ELECTRICAL CHARACTERISTICS (T_C=25°C unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C =-25mA; I _B = 0	-120	--	--	V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -5A; I _B = -0.5A	--	--	-1.5	V
V _{BE(sat)}	Base -Emitter Saturation Voltage	I _C = -5A; I _B = -0.5A	--	--	-2.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -120V; I _E = 0	--	--	-50	uA
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0	--	--	-50	uA
h _{FE1}	DC Current Gain	I _C = -1A; V _{CE} = -5V	60	--	320	
h _{FE2}	DC Current Gain	I _C = -5A; V _{CE} = -5V	20	--	--	
Cob	Collector output capacitance	V _{CB} =-10V ,I _E =0,f=1MHz		200		nF
f _T	Transition frequency	V _{CE} =-5V ,I _C =-1A		65		MHz

Product Disclaimer

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