

Silicon NPN Power Transistors

2SC3284

DESCRIPTION

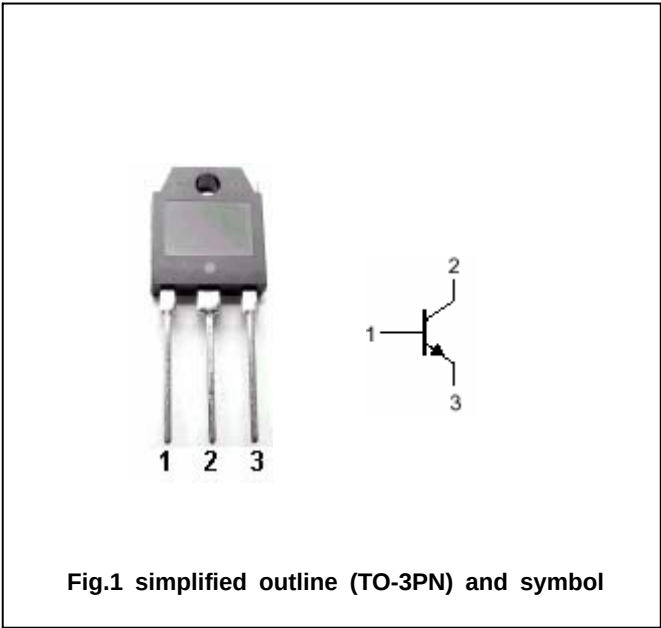
- With TO-3PN package
- Complement to type 2SA1303

APPLICATIONS

- Audio and general purpose

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter



Absolute maximum ratings(Ta=°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	150	V
V _{CEO}	Collector-emitter voltage	Open base	150	V
V _{EBO}	Emitter-base voltage	Open collector	5	V
I _C	Collector current		14	A
I _B	Base current		3	A
P _C	Collector power dissipation	T _C =25°C	125	W
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-55~150	°C

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =25mA ; I _B =0	150			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =5A I _B =0.5A			2.0	V
I _{CBO}	Collector cut-off current	V _{CB} =150V I _E =0			100	μA
I _{EBO}	Emitter cut-off current	V _{EB} =5V; I _C =0			100	μA
h _{FE}	DC current gain	I _C =5A ; V _{CE} =4V	50		180	
C _{ob}	Output capacitance	I _E =0 ; V _{CB} =10V; f=1MHz		200		pF
f _T	Transition frequency	I _E =-2A ; V _{CE} =12V		60		MHz

Switching times

t _{on}	Turn-on time	I _C =5A; R _L =12Ω I _{B1} =- I _{B2} =0.5A V _{CC} =60V		0.20		μs
t _s	Storage time			1.50		μs
t _f	Fall time			0.35		μs

◆ h_{FE} Classifications

O	P	Y
50-100	70-140	90-180

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PACKAGE OUTLINE

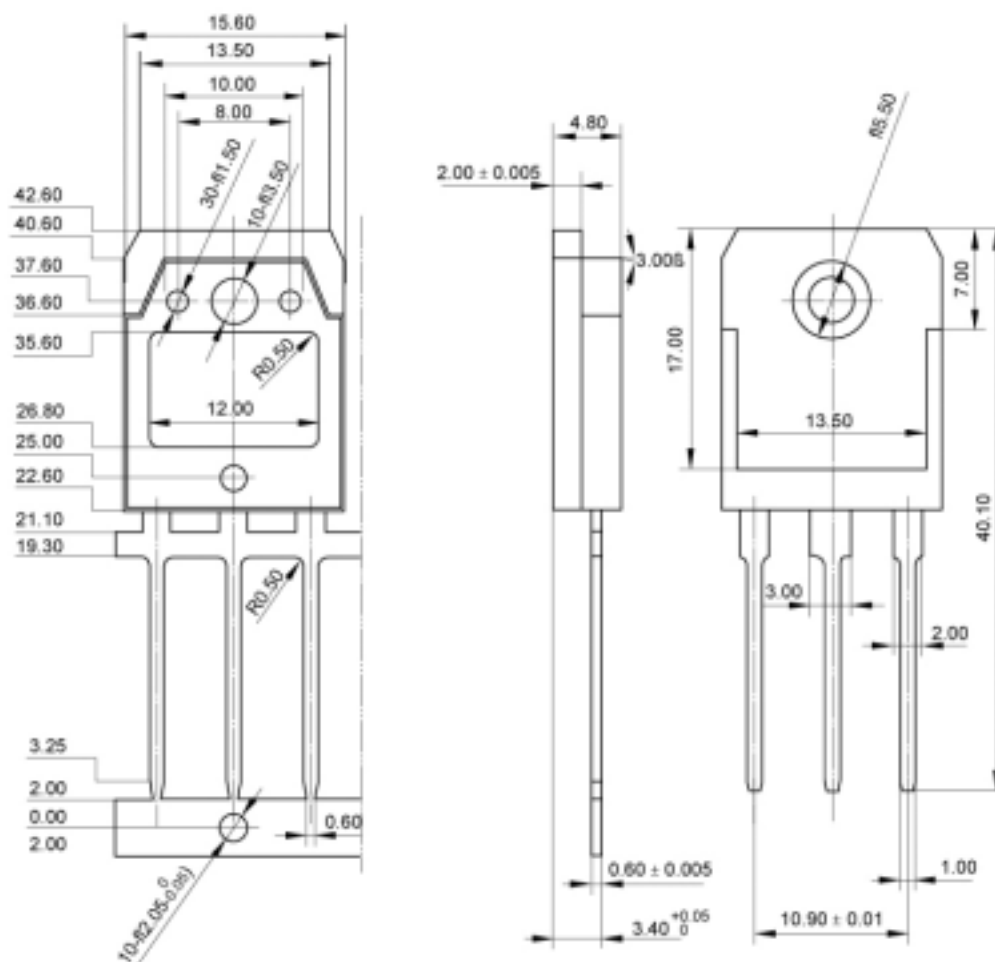


Fig.2 outline dimensions (unindicated tolerance: $\pm 0.1\text{mm}$)

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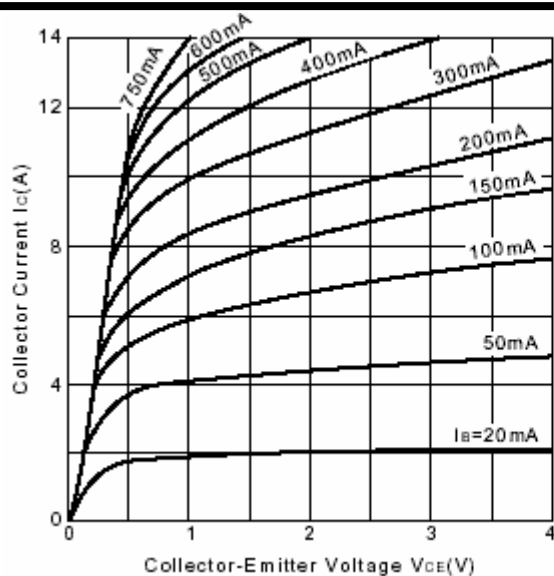


Fig.3 Static Characteristic

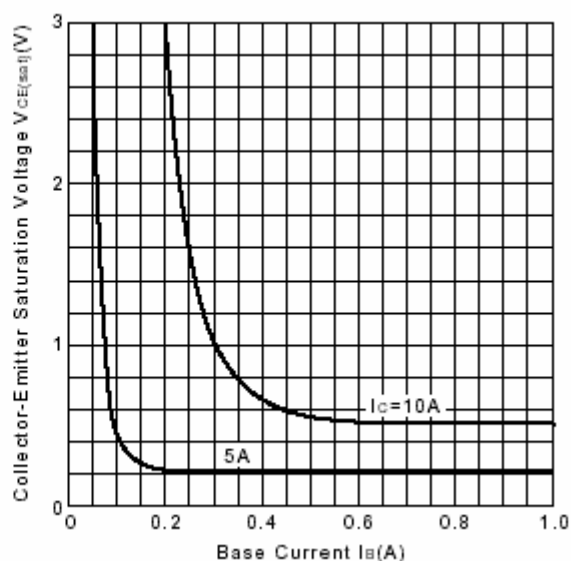
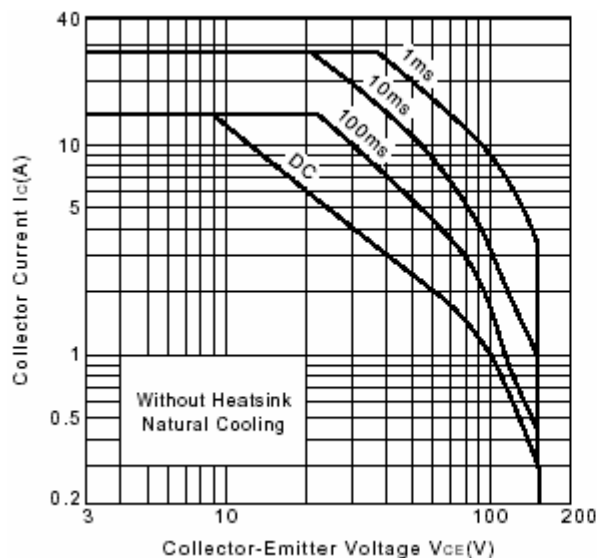
Fig.4 $V_{CE(sat)}-I_B$ 

Fig.5 Safe Operating Area

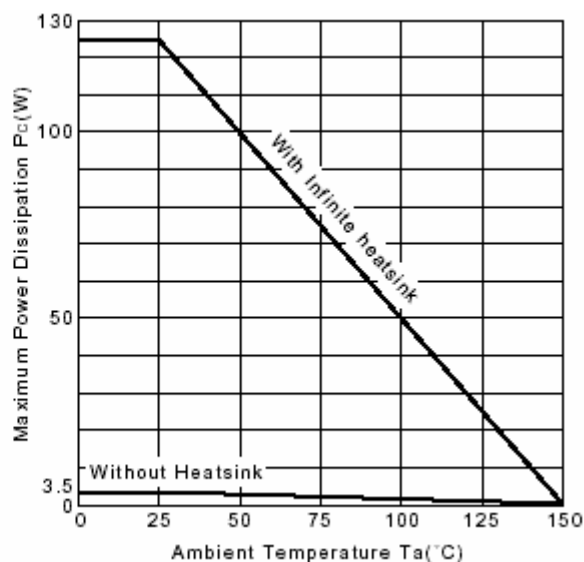


Fig.6 Power Derating

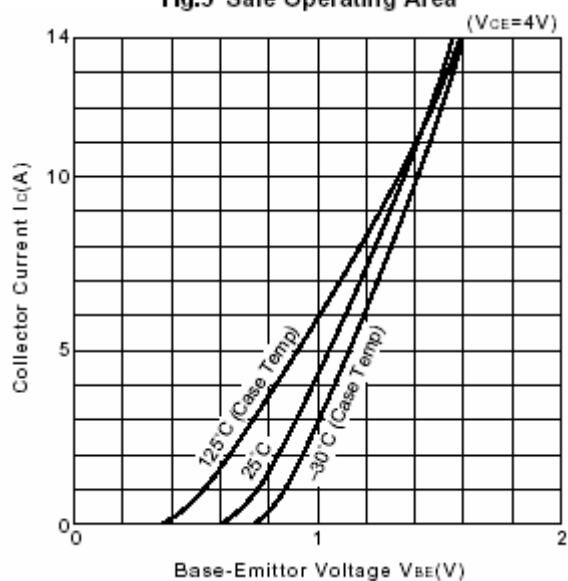
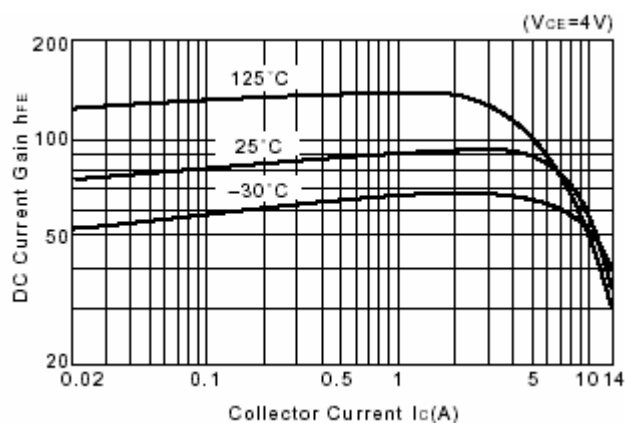
Fig.7 I_C-V_{BE} 

Fig.8 DC current Gain