

Silicon NPN Power Transistors

2SD1941

DESCRIPTION

- With TO-3PFM package
- High voltage ,high speed
- Low collector saturation voltage

APPLICATIONS

- Color TV horizontal output applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

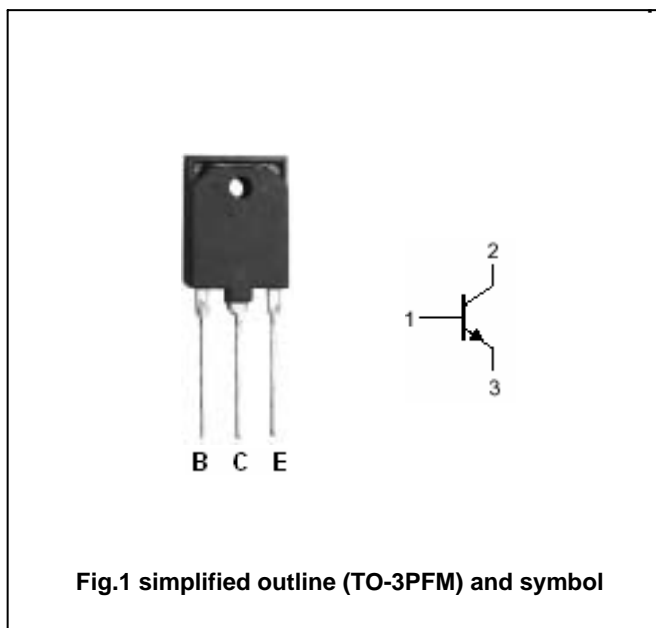


Fig.1 simplified outline (TO-3PFM) and symbol

Absolute maximum ratings (Ta=25 )

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-base voltage	Open emitter	1500	V
$V_{CEO}$	Collector-emitter voltage	Open base	600	V
$V_{EBO}$	Emitter-base voltage	Open collector	5	V
$I_C$	Collector current		6	A
$I_B$	Base current		3	A
$P_C$	Collector power dissipation	$T_C=25$	50	W
$T_j$	Junction temperature		150	
$T_{stg}$	Storage temperature		-55~150	

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## CHARACTERISTICS

T<sub>j</sub>=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CEO(SUS)</sub>	Collector-emitter sustaining voltage	I <sub>C</sub> =0.1A, I <sub>B</sub> =0	600			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =5A; I <sub>B</sub> =1A		3.0	5.0	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =5A; I <sub>B</sub> =1A			1.5	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =800V; I <sub>E</sub> =0			10	μA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =5V; I <sub>C</sub> =0			1	mA
h <sub>FE-1</sub>	DC current gain	I <sub>C</sub> =1A; V <sub>CE</sub> =5V	8		36	
h <sub>FE-2</sub>	DC current gain	I <sub>C</sub> =5A; V <sub>CE</sub> =5V	5			

固电半导体

INCHANGE SEMICONDUCTOR

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PACKAGE OUTLINE

