

MSI/LSI, DIGITAL, DIGILIN & LINEAR CIRCUITS

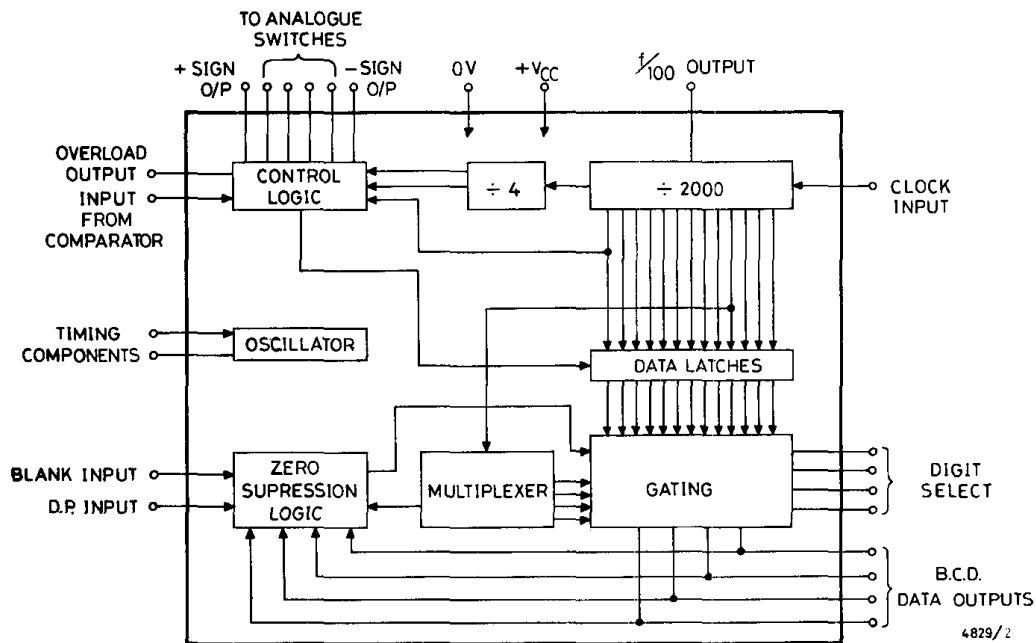
3½ DIGIT D.V.M. LOGIC

ZNA116E

The ZNA116E allows a precision 3½ digit D.V.M. to be constructed very easily. It provides all the control logic necessary for a D.V.M. using the well known dual slope integration technique. The low power requirements of the device make it attractive for portable battery operated applications and, since it is bipolar, requires no special handling precautions. The device is contained within a 24 lead plastic D.I.L.

FEATURES

- 3½ Decade display (± 1999 max. reading)
- Automatic polarity detection and indication
- Leading zero suppression
- Overload indication
- Multiplexed B.C.D. outputs capable of driving a T.T.L. decoder/ driver directly (e.g. ZN7447A)
- External input to blank display
- Internal oscillator, adjustable externally
- An output at $1/100$ clock frequency for under range indication, or for synchronising the clock frequency for optimum mains rejection
- Single rail, 5 volt supply operation (at 10 mA typical)



System Diagram