

SURFACE MOUNT SUPER FAST RECTIFIER

ES2A - ES2J



DO-214AA(SMB)
Surface Mount Package

Polarity: Colour band denotes Cathode end

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(Rating at 25°C Ambient Temperature unless specified otherwise. Single Phase, half wave 60Hz, Resistive or Inductive Load, for Capacitive Load current derate by 20%)

DESCRIPTION	SYMBOL	ES2A	ES2B	ES2C	ES2D	ES2E	ES2G	ES2J	UNIT	
Maximum Peak Repetitive Reverse Voltage	V_{RRM}	50	100	150	200	300	400	600	V	
Maximum RMS Voltage	V_{RMS}	35	70	105	140	210	280	420	V	
Maximum DC Blocking Voltage	V_{DC}	50	100	150	200	300	400	600	V	
Maximum Average Forward Rectified Current at $T_L=55^\circ\text{C}$	$I_{(AV)}$	2.0							A	
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load	I_{FSM}	50							A	
Maximum Instantaneous Forward Voltage at $I_F=2.0\text{A}$	V_F	0.95				1.25		1.7	V	
Maximum DC Reverse Current $T_a=25^\circ\text{C}$ at Rated DC Blocking Voltage $T_a=100^\circ\text{C}$	I_R	5.0				50				μA
Maximum Reverse Recovery Time	t_{rr}	35							ns	
Typical Junction Capacitance	C_J	60							pF	
Thermal Resistance Junction to Ambient	$R_{th(j-a)}$	TYP40							$^\circ\text{C/W}$	
Operating Junction and Storage Temperature Range	T_j, T_{stg}	- 65 to +150							$^\circ\text{C}$	

*Reverse recovery condition $I_F=0.5\text{A}$, $I_R=1\text{A}$, $t_{rr}=0.25\text{A}$

**Measured at 1MHz and applied reverse voltage of 4.0V D.C

*** Mounted on P.C.B with "0.2 x 0.2" (5 x 5mm) Copper Pad Areas

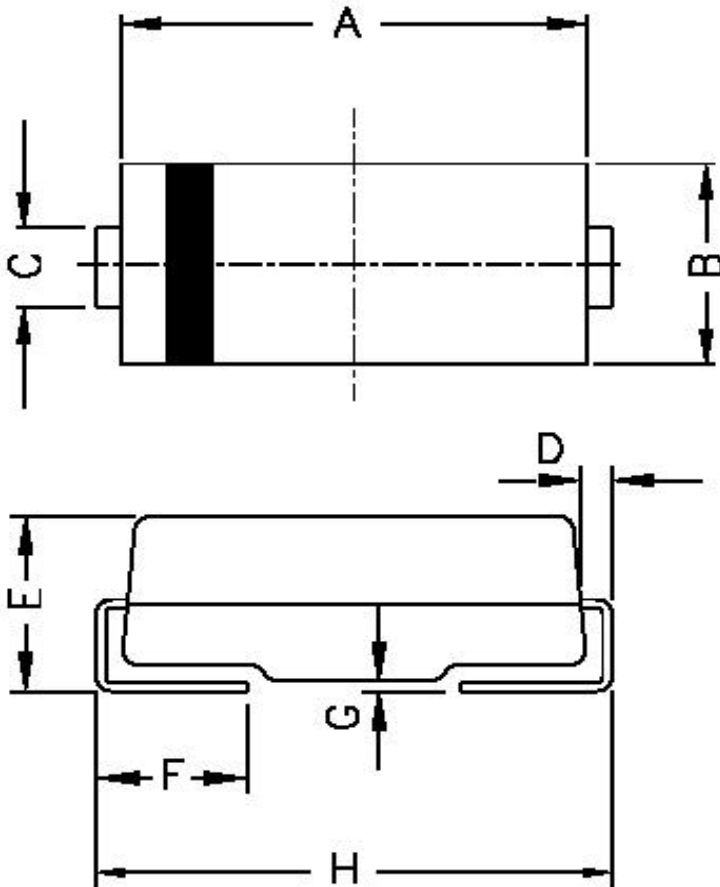
ES2A_2J Rev300310E

ES2A - ES2J

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PACKAGE DO 214AA(SMB)



DIM	MIN.	MAX.
A	4.05	4.75
B	3.30	3.95
C	1.95	2.20
D	0.15	0.31
E	1.95	3.12
F	0.75	1.60
G	0.050	0.203
H	5.10	5.60

DIMENSIONS ARE IN mm

Cathode is marked by Band.

Packing: 3K/ Per Reel

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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